

**REMARKS**

Claims 1, 4-7 and 11-19 are pending. The Examiner's reconsideration of the rejections is respectfully requested in view of the amendments and remarks.

Applicants appreciate the Examiner's indication that claims 3, 5-7, 11-13 and 15-19 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in the Office Action and to include all of the limitations of the base claim and any intervening claims.

Applicants note that certain documents contained in the information disclosure statement filed on September 24, 2004 have not been considered. The Examiner stated essentially that the information disclosure statement filed to comply with 37 CFR 1.98(a)(1). The indication found on the form PTO-1449 states essentially that the English abstracts have not been considered because no document numbers or article's name or authors are listed.

Applicants respectfully request the consideration of the English abstract documents.

Identification of references under 37 CFR 1.98(b) pertains to "patents" and "publications" and does not require the identification of English abstracts by author, title, etc. Further, the information disclosure statement indicates that the English abstracts pertain to the cited Japanese patents identified by document numbers. More particularly, the asterisks "\*\*\*\*" call attention to the relationship between the Japanese patents and the provided English abstracts. Thus, the English abstracts are identified by the corresponding Japanese patent document numbers.

Accordingly, consideration of the documents is respectfully requested.

Claims 1-7 and 11-19 have been rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which

applicant regards as the invention. The Examiner stated essentially that the term "direct current voltage signal" is unclear.

Applicants appreciate the Examiner's suggestion to amend "a direct current voltage signal" to "a direct current signal" or "a direct voltage signal". Respectfully, there is believed to be no confusion in reciting "a direct current voltage signal"; for example, it would be clear to one skilled in the art that voltages can be of the DC type or AC type. Thus, the recitation of a "direct current voltage signal" clearly refers to a voltage signal of the DC variety. Similarly, an "alternating current voltage signal" refers to a voltage signal of the AC variety. To further clarify the claims the phrase "voltage signal" has been amended to "voltage-signal." Thus, Applicants believe that the terms "direct current voltage-signal" and "alternating current voltage-signal" to be clear. The claims are believed to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The Examiner's reconsideration of the rejection is respectfully requested.

Claims 1, 2, 4, and 14 have been rejected under 35 U.S.C. 102(b) as being anticipated by Penrod (U.S. Patent No. 3,993,984). The Examiner stated essentially that Penrod teaches all the limitations of claims 1, 2, 4, 10 and 14.

Claim 1 claims, *inter alia*, "wherein the digital detector comprises a transistor for attenuating a component of the digital signal having a predetermined logic level when the digital signal is oscillated at a frequency greater than or equal to a predetermined frequency."

Claim 1 has been amended to include the allowable limitations of claim 3 and the limitations of intervening claim 2. Therefore, claim 1 is believed to be allowable over Penrod.

Claims 4 and 14 depend from claim 1. Claim 2 has been cancelled.

Claims 1, 2, 4 and 14 have been rejected under 35 U.S.C. 102(b) as being anticipated by Suga (U.S. Patent No. 4,468,796). The Examiner stated essentially that Suga teaches all the limitations of claims 1, 2, 4, 10 and 14.

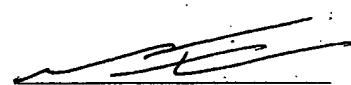
Claim 1 claims, *inter alia*, "wherein the digital detector comprises a transistor for attenuating a component of the digital signal having a predetermined logic level when the digital signal is oscillated at a frequency greater than or equal to a predetermined frequency."

Claim 1 has been amended to include the allowable limitations of claim 3 and the limitations of intervening claim 2. Therefore, claim 1 is believed to be allowable over Suga.

Claims 4 and 14 depend from claim 1. Claim 2 has been cancelled.

For the forgoing reasons, the present application, including claims 1, 4-7 and 11-19, is believed to be in condition for allowance. The Examiner's early and favorable action is respectfully urged.

Respectfully submitted,



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